

Notice of References Cited

Application/Control No.

10/092,308

Applicant(s)/Patent Under
Reexamination
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Examiner

Thao X Le

Art Unit

2814

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-5,557,141	09-1996	Harada et al.	257/14
	B	US-6,392,278	05-2002	Kimura, Shingo	257/202
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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Silicon Processing for VLSI Era, Volume 1 - Process Technology by S. Wolf and R.N. Tauber, pages 407-409
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)

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